

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant	: Isamu Kobori, et al.	Art Unit	: 2822
Serial No.	: 10/713,275	Examiner	: Kiesha L. Rose
Filed	: November 17, 2003		
Title	: METHOD OF MANUFACTURING A SEMICONDUCTOR METHOD OF MANUFACTURING A THIN-FILM TRANSISTOR AND THIN-FILM TRANSISTOR		

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT IN REPLY TO ADVISORY ACTION OF FEBRUARY 3, 2006

Please amend the application as indicated on the following pages.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 7 of this paper.